

### **REMARKS**

Claims 20-24 remain in the application for consideration of the Examiner with Claims 1-19 standing cancelled.

Reconsideration and withdrawal of the outstanding objections and rejections are respectfully requested in light of the above amendments and following remarks.

The drawings were objected to.

By the instant amendment, Claim 1 has been amended to take into consideration the concerns of the Examiner.

This objection is traversed in part.

Furthermore, the Examiner alleges that M20 and M21 has a positive voltage V+ applied to the gates and MOS transistors M24 and M25 have a negative V applied their gates.

However, Applicants traverse this allegation since M20 and M21 has Vin+ applied to their gates while M24 and M25 have Vin- applied to their gates.

Claims 20-24 were rejected under 35 U.S.C. § 112, second paragraph, as being indefinite.

These rejections are respectfully traversed.

The Examiner alleges that the first input voltage signal and the second input voltage signal both have the same value namely V+.

However, Applicants submit that the first input voltage signal and the second input voltage signal refer to  $V_{in+}$  and  $V_{in-}$ .

The second allegation that circuitry is inappropriate, has been addressed by the instant amendment to Claim 20.

The remaining rejections all appear to be related to the concern with respect to the first input voltage signal and the second voltage signal. Consequently, the above remarks correct those.

It is respectfully submitted that Claims 20-24 are in full compliance with 35 U.S.C. § 112 and particularly points out and distinctly claims the subject matter which Applicants believe is their invention.

Applicants appreciate the indication that if Claims 20-24 were rewritten or amended to overcome the rejection under 35 U.S.C. § 112, second paragraph, these claims would be allowable.

Applicants respectfully submit that the instant amendment has overcome the rejection under 35 U.S.C. § 112, and consequently Claims 20-24 are allowable.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant response places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,



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